

Trigger/Clock Distribution Module

Overview

The trigger/clock distribution module consists of two independent channels of a comparator driving an ECL line driver and eight TTL line drivers. Each comparator has both an on-board threshold level adjustment and a hysteresis adjustment.

This module is designed to capture an input signal on each of two channels and distribute the signals to other devices. Applications include distributing standard clock and other signals in laboratory, factory, and general ATE requirements.

Each input signal is passed through a high-speed comparator to convert analog type signals into logic level signals with a minimum propagation delay. Each output is individually buffered to prevent signal interactions. The module is housed in a C-size VXIbus enclosure and derives all necessary power from the VXIbus.

Specifications

Input Connector:	SMB jack
Input Voltage Range:	+5 V to -5 V
Input Impedance:	50 Ω
Input Threshold:	+2.5 V to -2.5 V (requires removing cover to adjust). Factory preset to +2 V
Input Hysteresis:	100 mV to 500 mV (requires removing cover to adjust). Factory preset to 300 mV
ECL Output Connector:	15 pin D-Sub, female
ECL Output Characteristics:	10 k series ECL, differential signal pulled down to -5.2 V with 499 Ω
TTL Output Connector:	SMB jack
TTL Output Impedance:	12.5 Ω
TTL Output Termination:	TTL signals should be forward terminated into 50 Ω to ground
TTL Output Levels:	Open circuit, Low: < 0.4 V, High: > +4.6 V 50 Ω term. Low: < 0.4 V, High: > +3.6 V



Features

- Single-slot VXIbus Module
- 2 Input Comparator Channels
- 8 TTL & 1 ECL Trigger Outputs per Module
- Outputs Individually Buffered
- Ideal for Distributing Triggers or System Clocks
- Synchronize Multiple Slot 0s or Instruments

Ordering Information

CSM-S-11056 Trigger/Clock Distribution Module

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